



THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s) Tremblay, Marc; Joy, William

Assignee: Sun Microsystems, Inc.

Title: Implicitly Derived Register Specifiers In A Processor

Serial No.: 09/204,479

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entered

Newport Beach, California

April 3, 2001

BOX CPA
COMMISSIONER FOR PATENTS
Washington, D. C. 20231

**PRELIMINARY AMENDMENT/RESPONSE TO FINAL OFFICE ACTION DATED
JANUARY 3, 2001**

Dear Sir:

In response to the Final Office Action dated January 3, 2001, please amend this application as follows:

Technology Center 2100

APR 09 2001

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IN THE CLAIMS

1. A processor comprising:

a register file including a plurality of registers;

a functional unit coupled to the register file, the functional unit that executes an instruction operating upon a plurality of registers in the register file, the instruction in which a register specifier is implicitly derived, based on another register specifier;

a decoder coupled to the functional unit and coupled to the register file, the decoder implicitly deriving a register specifier based on an explicitly-specified register specifier of the instruction.